

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Canceled)
2. (Previously Presented) The method of claim 8 wherein said generating the motion estimation data includes generating a motion vector indicative of a location of blocks of pixels in a current frame relative to a previous frame.
3. (Previously Presented) The method of claim 8 wherein said temporally sub-sampling the video frames includes temporally sub-sampling the video frames once to a frame rate that is common to all video outputs.
4. (Previously Presented) The method of claim 8 wherein said temporally sub-sampling the video frames includes temporally sub-sampling the video frames in a hierarchical manner, including progressively sub-sampling previously sub-sampled video frames to lower frame rates, with each frame rate corresponding to a different video output.
5. (Previously Presented) The method of claim 8 wherein said converting the color format of the video frames includes converting the color format once to a color format that is common to all video outputs.
6. (Previously Presented) The method of claim 8 wherein said converting the color format of the video frames includes converting the color format in a hierarchical manner to multiple color formats, including progressively converting color formats of video

frames having previously converted color formats, with each color format corresponding to a different video output.

7. (Canceled)

8. (Currently Amended) A method to encode multiple video outputs from a common input video source, the method comprising:

temporally sub-sampling video frames of the common input video source to reduce a frame rate;

converting a color format of the video frames from a first format to a second format;

performing, by a hardware processor, hierarchical Human Visual System (HVS)-based pre-processing to filter high frequency information from the video frames and to generate sequences of video frames having different bit rates;

performing full motion estimation on a first sequence of video frames and generating motion estimation data to substantially match blocks of pixels from one video frame to another video frame in the first sequence;

generating a first video output using the generated motion estimation data;

reusing the generated motion estimation data to perform partial motion on at least a second sequence of video frames to substantially match blocks of pixels from one video frame to another video frame in the second sequence and modifying the generated motion estimation data to correspond to blocks of pixels in the second sequence that match; and

generating a second video output using the modified motion estimation data.

9. (Canceled)

10. (Previously Presented) The article of manufacture of claim 12 wherein said output produced from the first encoder component includes produced full motion estimation data, and wherein using the output produced from the first encoder component in the second

encoder component includes using in said second encoder component at least some of the produced full motion estimation data to locate an approximate location of a block of pixels in a frame without performing full motion estimation.

11. (Original) The article of manufacture of claim 10 wherein the motion estimation data includes motion vector information.

12. (Previously Presented) An article of manufacture, comprising:  
a computer-readable medium having computer-readable instructions stored thereon that are executable by a processor to cause said processor to encode multiple video outputs from common input video data, by:

temporally sub-sampling video frames of the common input video source to reduce a frame rate;

converting a color format of the video frames from a first format to a second format;

determining data producer and data user relationships between encoder components;

directing output produced from a first encoder component to a second encoder component in a same first encoder;

directing the output produced from the first encoder component to another encoder component in a second encoder different from the first encoder;

using the output produced from the first encoder component in the second encoder component;

hierarchically Human Visual System (HVS)-based pre-processing the video frames to obtain different bit rates and to remove high frequency information from the video frames; and

producing output from the second encoder that is modified from output produced by the first encoder.

13. (Previously Presented) The article of manufacture of claim 12 wherein the computer-readable medium further has computer-readable instructions stored thereon that are executable by said processor to cause the processor to encode multiple video outputs from common input video data, by:

adapting the hierarchical HVS-based pre-processing based on outputs from subsequent encoder component processing.

14. (Canceled)

15. (Previously Presented) The apparatus of claim 18 wherein the hierarchical arrangement of encoder components further includes:

a hierarchical arrangement of motion estimation units corresponding to different encoders;

wherein a first unit of the hierarchical arrangement of motion estimation units performs full motion estimation on a first sequence of video frames and generates motion estimation data to substantially match blocks of pixels from one video frame to another video frame in the first sequence; and

wherein a second unit of the hierarchical arrangement of motion estimation units uses the generated motion estimation data to perform partial motion estimation on at least a second sequence of video frames to substantially match blocks of pixels from one video frame to another video frame in the second sequence and modifies the generated motion estimation data to correspond to blocks of pixels in the second sequence that match.

16. (Previously Presented) The apparatus of claim 15, further comprising a final encoder component for each of the encoders, the final encoder components including a motion compensation element to use modified motion estimation data produced by respective said first and second units to re-generate a video frame.

17. (Previously Presented) The apparatus of claim 18 wherein either one or both of the temporal sub-sampling and color format conversion components performs their respective operations once for all of the multiple video outputs.

18. (Previously Presented) An apparatus to encode multiple video outputs from common input video data, the apparatus comprising:

a temporal sub-sampling component to temporally sub-sample video frames of the common input video data to a lower frame rate;

a color format conversion component to convert the video frames from a first format to a second format;

a hierarchical arrangement of encoder components wherein output data of at least some of the encoder components are shared as input data to other encoder components of different encoders; and

a negotiation block to determine data producer and data user relationships between the encoder components and to control flow of the input and output data of the encoder components,

wherein the hierarchical arrangement of encoder components includes a hierarchical arrangement of Human Visual System (HVS)-based pre-processing filters to reduce high frequency information in video frames and to change bit rates of the video frames, wherein output from one of the HVS-based pre-processing filters of a first encoder are used as input to another one of the HVS-based pre-processing filters of a second encoder.

19. (Original) The apparatus of claim 18 wherein the HVS-based pre-processing filters are adaptive in response to outputs of another encoder component in their same encoder.

20. (Canceled)

21. (Previously Presented) The system of claim 23 wherein the means for hierarchically sharing data further includes means for sharing motion estimation data between different encoders.

22. (Previously Presented) The system of claim 23, further comprising:  
a means for receiving the common video data; and  
means for transmitting the multiple video outputs to terminal devices.

23. (Previously Presented) A system for encoding multiple video outputs from common input video data, the system comprising:

means for temporally sub-sampling video frames of the common input video to a lower frame rate;

means for converting a color format of the video frames from a first format to a second format;

means for hierarchically sharing data output from encoder components, wherein output data of at least some of the encoder components can be shared as input data to other encoder components of different encoders; and

means for determining data producer and data user relationships between the encoder components and for determining flow of the input and output data of the encoder components,

wherein the means for hierarchically sharing data includes means for hierarchically Human Visual System (HVS)-based pre-processing video frames.

24. (Canceled)

25. (Previously Presented) The system of claim 27 wherein the hierarchical arrangement of encoder components further includes a hierarchical arrangement of motion estimation and macroblock mode units.

26. (Previously Presented) The system of claim 27 wherein the common input video data includes raw video data.

27. (Previously Presented) A system to produce multiple video outputs from common input video data, the system comprising:

an input point at which to receive common input video data for a single encoding session;

an encoder having a hierarchical arrangement of encoder components, wherein output data of at least some of the encoder components is shared as input data to other encoder components of different encoders;

a negotiation block to determine data producer and data user relationships between the encoder components and to control flow of the input and output data of the encoder components; and

a plurality of output points from the hierarchical arrangement of encoder components to output a corresponding plurality of output video that has been processed by the encoder components during the single encoding session,

wherein the hierarchical arrangement of encoder components includes a hierarchical arrangement of human visual system (HVS)-based filters.

28. (Previously Presented) The system of claim 27 wherein at least some of the hierarchical arrangement of encoder components include software components.